APPLICATION # 10/725,593

| L4 | 0 | ((a display adj device) with (level adj converting adj circuit) with (tft or thin adj film adj transistor$1) with (substrate$1) and (level adj converting adj circuit) with (non$1differential adj input adj signal$1) same (amplitude "1.2V or less") with (large adj amplitude adj signal$1) and (input adj signal$1) with (control$3 adj signal$1) with (first adj conductive adj type) adj (first adj transistor) with connect$3 with (first adj power adj source adj line) same (first adj voltage) with (output adj terminal) and (gate adj electrode) with (input adj signal$1) with (first adj capacitive adj element) and (second adj conductive adj type) with (second adj transistor) with (connect$3) with (output adj terminal) with (second adj power adj source adj line$1) same (second adj voltage) and (gate adj electrode) with (input adj signal$1) with (second adj capacitive adj element) and (first adj bias adj circuit) with (first adj bias adj voltage) same (gate adj electrode) with (first adj transistor) and (second adj bias adj circuit) with (second adj bias adj voltage) same (gate adj electrode) with (second adj transistor) and (first adj bias adj voltage) same (voltage adj turn$1 adj off) and (first adj transistor) same (voltage) with (gate adj electrode) same (first adj transistor) with (maximum adj value$1) and (second adj bias adj voltage) with (voltage adj turn$1 off) same (second adj transistor) same (voltage) with (gate adj electrode$1) with (second adj transistor) with (minimum adj value) and (first adj switch$3) with (first adj electrode) with connect$3 with (first adj power adj source adj line$1) with (first adj voltage$1) and (second adj switch$3 adj element) with (second adj electrode$1) with connect$3 with (second adj electrode$1) with (first adj switch$3 adj element) and (first adj electrode) with connect$3 with | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/05/27 23:22 |

((a display adj device) with (level adj converting adj circuit) with (tft or thin adj film adj transistor$1) with (substrate$1) and (level adj converting adj circuit) with (non$1differential adj input adj signal$1) same (amplitude "1.2V or less") with (large adj amplitude adj signal$1) and (input adj signal$1) with (control$3 adj signal$1) with (first adj conductive adj type) adj (first adj transistor) with connect$3 with (first adj power adj source adj line) same (first adj voltage) with (output adj terminal) and (gate adj electrode) with (input adj signal$1) with (first adj capacitive adj element) and (second adj conductive adj type) with (second adj transistor) with (connect$3) with (output adj terminal) with (second adj power adj source adj line$1) same (second adj voltage) and (gate adj electrode) with (input adj signal$1) with (second adj capacitive adj element) and (first adj bias adj circuit) with (first adj bias adj voltage) same (gate adj electrode) with (first adj transistor) and (second adj bias adj circuit) with (second adj bias adj voltage) same (gate adj electrode) with (second adj transistor) and (first adj bias adj voltage) same (voltage adj turn$1 adj off) and (first adj transistor) same (voltage) with (gate adj electrode) same (first adj transistor) with (maximum adj value$1) and (second adj bias adj voltage) with (voltage adj turn$1 off) same (second adj transistor) same (voltage) with (gate adj electrode$1) with (second adj transistor) with (minimum adj value) and (first adj switch$3) with (first adj electrode) with connect$3 with (first adj power adj source adj line$1) with (first adj voltage$1) and (second adj switch$3 adj element) with (second adj electrode$1) with connect$3 with (second adj electrode$1) with (first adj switch$3 adj element) and (first adj electrode) with connect$3 with (second adj electrode) with tranistor and (third adj transistor) with (gate adj electrode) same (output adj voltage) with (sample adj holding adj circuit) and (second adj switch$3 adj element) with (second adj electrode$1) with connect$3 with (second adj electrode$1) with (first adj switch$3 adj element) and (first adj electrode) with connect$3 with (second adj electrode) with (third adj transistor) and (inverter adj circuit) with (first adj power adj source adj line$1) with (second adj power adj source adj line$1) and (output adj voltage) with (voltage adj holding adj circuit) with (input$4) with (inverter adj circuit) and (third adj bias adj circuit)).clm.